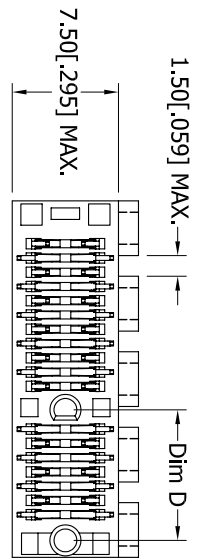
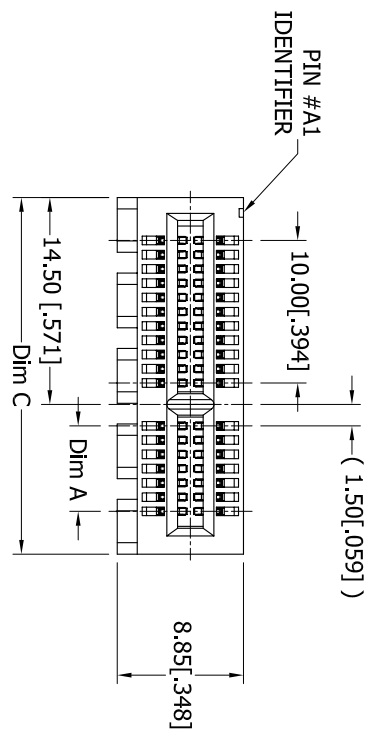
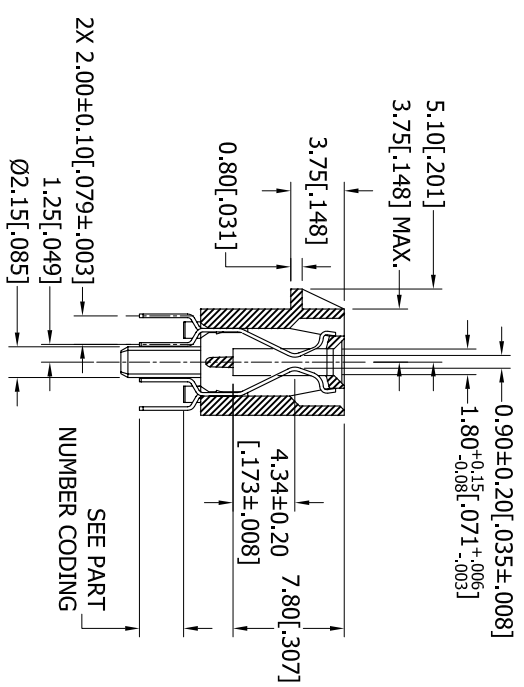
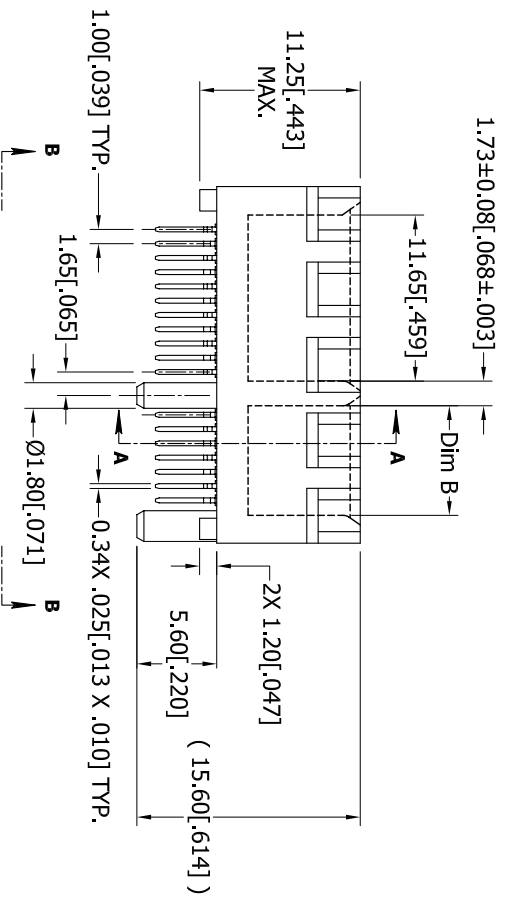


REV.		ECO. NO	DESCRIPTION	DATE	BY
A	1595		INITIAL RELEASE	11/31/07	VJ
B	1707		UPDATE P/N CODING CHART, PLATING	05/30/08	ASK



VIEW B-B



SECTION A-A

PART NUMBER	POSITIONS/ CONTACTS	A		B		C		D		LINK WIDTH CONNECTOR
		MM	INCH	MM	INCH	MM	INCH	MM	INCH	
N.E18DHHN-T9	18/36	6	0.236	7.65	0.301	25	0.984	9.15	0.360	X1
N.E32DHHN-T9	32/64	20	0.787	21.65	0.852	39	1.535	23.15	0.911	X4
N.E49DHHN-T9	49/98	37	1.457	38.65	1.522	56	2.205	40.15	1.581	X8
N.E82DHHN-T9	82/164	70	2.756	71.65	2.821	89	3.504	73.15	2.880	X16

UNLESS OTHERWISE SPECIFIED, DIMENSIONS ARE IN MILLIMETERS (INCHES).
 TOLERANCES:
 ANGULAR: ±.30°
 HOLE POSITION: ±.05 (±.002)
 HOLE DIA: ±.01 (±.0004)
 SURFACE FINISH: 63 RA
 SURFACE FINISH: 12.5 RA
 INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

11. CONNECTOR IDENTIFICATION: THE PART SHALL BE MARKED WITH A PART NUMBER AND BARCODE.
 12. BOARD THICKNESS ACCOMMODATED: 1.57±0.20 [.062±.008].
 13. BOARD INSERTION FORCE: 1.15N (117.3)g MAX PER CONTACT PAIR WHEN USING A 1.57 [.062] TEST BLADE.
 14. BOARD WITHDRAWAL FORCE: 0.15N (15.31)g MINIMUM PER CONTACT PAIR USING 1.57 [.062] PCB
 15. MODIFICATION: SEE PART NUMBER CODING.

10. INSULATION RESISTANCE: 1000 MEGA OHMS.
 9. VOLTAGE DROP: 44 MILLI VOLT AT RATED CURRENT.
 8. CURRENT RATING: 1.1 AMP PER CONTACT PAIR.
 7. VOLTAGE RATING: 50 VDC MINIMUM AT SEA LEVEL.
 6. UL FLAMMABILITY RATING: 94V-0
 5. PROCESSING TEMP: 260°C FOR 10 SECONDS MAX.
 4. OPERATING TEMPERATURE: -10°C ~ 85°C.
 3. PLATING: SEE PART NUMBER CODING.
 2. CONTACT MATERIAL: HIGH TEMPERATURE THERMOPLASTIC.
 1. INSULATOR MATERIAL: HIGH TEMPERATURE THERMOPLASTIC.

NOTES:
 1. INSULATOR MATERIAL: HIGH TEMPERATURE THERMOPLASTIC.
 2. CONTACT MATERIAL: SEE PART NUMBER CODING.
 3. PLATING: SEE PART NUMBER CODING.
 4. OPERATING TEMPERATURE: -10°C ~ 85°C.
 5. PROCESSING TEMP: 260°C FOR 10 SECONDS MAX.
 6. UL FLAMMABILITY RATING: 94V-0
 7. VOLTAGE RATING: 50 VDC MINIMUM AT SEA LEVEL.
 8. CURRENT RATING: 1.1 AMP PER CONTACT PAIR.
 9. VOLTAGE DROP: 44 MILLI VOLT AT RATED CURRENT.
 10. INSULATION RESISTANCE: 1000 MEGA OHMS.
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 14. BOARD WITHDRAWAL FORCE: 0.15N (15.31)g MINIMUM PER CONTACT PAIR USING 1.57 [.062] PCB
 15. MODIFICATION: SEE PART NUMBER CODING.

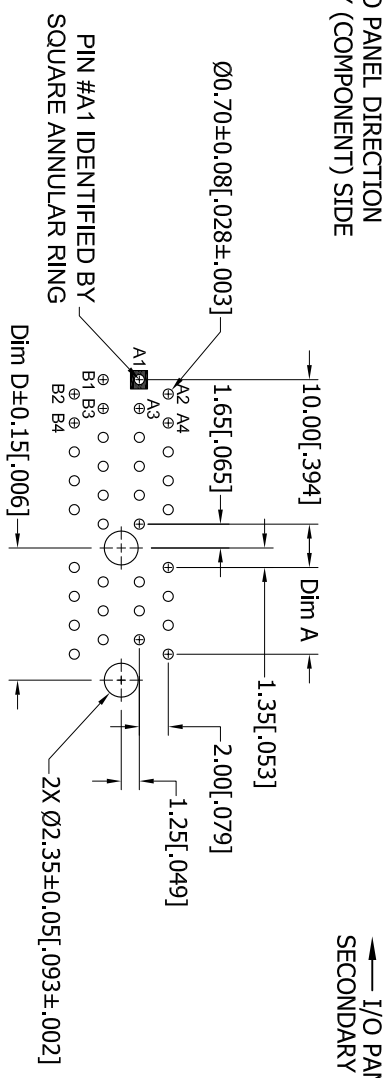
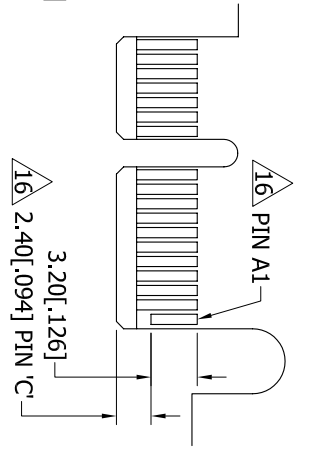
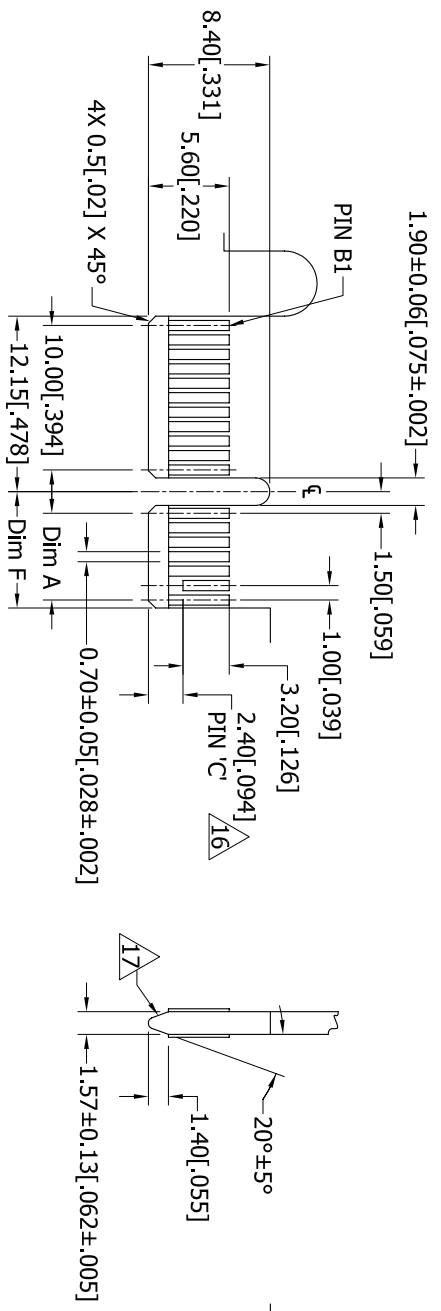
16. NO THE BAR PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS 'A1 AND 'C'.
 17. CHAMFER EDGES MUST BE FREE OF CUTTING BURR.

ROHS COMPLIANT

SCALE: 2:1

SHEET 1 OF 2

Z:\COMPONENTS\KPCOM\11045_N.E_DHHN-T9.dwg PRINT: 05/30/2008



PART NUMBER CODING

N _ E _ _ DHHN - T9 _ _

PLATING _____
 ALL PLATINGS ARE LEAD FREE AND HAVE .000050" NICKEL UNDERPLATE ON CONTACT SURFACE AND TERMINATION

CONTACT SURFACE _____
 B= .000010" GOLD
 C= .000030" GOLD
 W=GOLD FLASH

TERMINATION _____
 .000100" PURE TIN, MATTE
 .000100" PURE TIN, MATTE
 .000100" PURE TIN, MATTE

TAIL LENGTH _____
 1=2.30 [.091] }
 2=2.54 [.100] }
 3=3.00 [.118] - 2.36 [.093] PCB

COLOR _____
 1=BLACK
 2=BLUE
 3=GREEN
 4=PURPLE

POSITIONS _____
 18 (X1)
 32 (X4)
 49 (X8)
 82 (X16)



ROHS COMPLIANT

DAUGHTER CARD LAYOUT			
A	F	G	
MM	INCH	MM	INCH
6	0.236	8.15	0.321
20	0.787	22.15	0.872
37	1.457	39.15	1.541
70	2.756	72.15	2.840
			A1, B17, B31, B48, B81

UNLESS OTHERWISE SPECIFIED:
 DIMENSIONS ARE IN MILLIMETERS!
 TOLERANCES:
 ANGULAR: ± 30°
 HOLE POSITION: ± 0.05 (± 0.002)
 HOLE SIZE: ± 0.01 (± 0.0005)
 SURFACE FINISH: 63 IR
 SURFACE TREATMENT: 24HR
 REMOVE ALL BURRS AND SHARP EDGES 0.10 MAX
 INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

DATE	BY	CHKD	APP'D	REV
11/13/07	VJARA			

THE INFORMATION HEREIN CONTAINS PROPRIETARY INFORMATION OF SULLINS CONNECTOR SOLUTIONS. IT IS TO BE REPRODUCED, USED OR TRANSMITTED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF SULLINS CONNECTOR SOLUTIONS.

RESISTANCE TO VERTICAL THRU HOLE, PCIE PART NUMBER N _ E _ _ DHHN-T9 _ _

SIZE DWG. NO. 11045

SCALE: 2:1

SHEET 2 OF 2

